

Real Verification News

Economics of Verification

This issue of DACeZine also contains an article by [Carol Hallett and Jin Zhang](#) of Real Intent dealing with the economic factors that impact the choice of methods and tools used to verify a design. Companies deal with financial issues every time they choose a tool or a method, and yet, it is rare to find an article that covers this aspect: most published articles discuss the technology at the core of a particular EDA tool, and its benefit to the quality of the end product, but seldom is the issue of cost/benefit addressed explicitly. Yet, this is a subject that every engineering manager must address before he or she can obtain approval from the finance department to spend money to purchase licenses for a promising new product. <http://www.realintent.com/index.html>



Jin Zhang



Carol Hallett



Happy New Year

Real Intent introduces Meridian FPGA, CDC Verification Software for Altera Customers

San Jose, California – November 3, 2008- Real Intent Inc., the leading supplier of verification software for electronic design, announced its first release of Meridian FPGA™ verification software. Meridian FPGA is specifically designed to work with Altera Corporation's latest release of its Quartus® II software, version 8.1, to verify Clock Domain Crossings (CDC). It offers a cost-effective alternative when compared to the cost of equivalent ASIC design software.

<http://www.realintent.com/news/pr-2008/PR-nov-3-08.html>

MoSys picks Real Intent for IP verification, Timing Closure

SAN JOSE, CA -- (MARKET WIRE) -- Jan 14, 2009 -- [Real Intent](#) Inc., the leading supplier of verification software for electronic design, announced that [MoSys, Inc.](#) (NASDAQ: [MOSY](#)), a leading provider of high-density, high performance memory intellectual property (IP), has selected Real Intent's [Meridian CDC™ software](#) for the verification and sign-off of Clock Domain Crossings (CDC) for its semiconductor IP solutions.

<http://www.realintent.com/news/pr-2009/PR-jan-14-09.html>

Upcoming Events:

 **Electronic Design and Solution Fair 2009**
Jan 22- 23, 2009
PACIFICO YOKOHAMA, KANAGAWA, JAPAN

 **Design & Verification Conference & Exhibition**
Feb 24 & 26
DOUBLE TREE HOTEL, SAN JOSE

 **46th Design Automation Conference**
Jul 26 - 31, 2009
Booth 1728
MOSCONE CENTER, SAN FRANCISCO

Find the full articles on the web

www.realintent.com